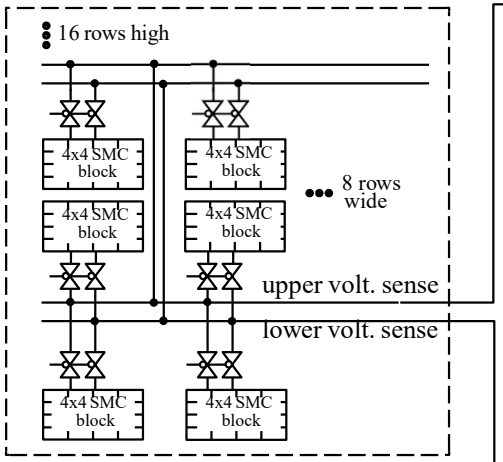
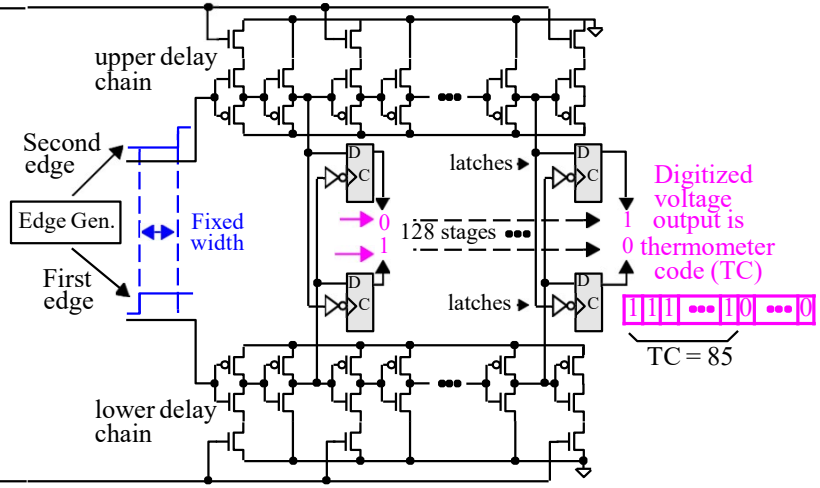
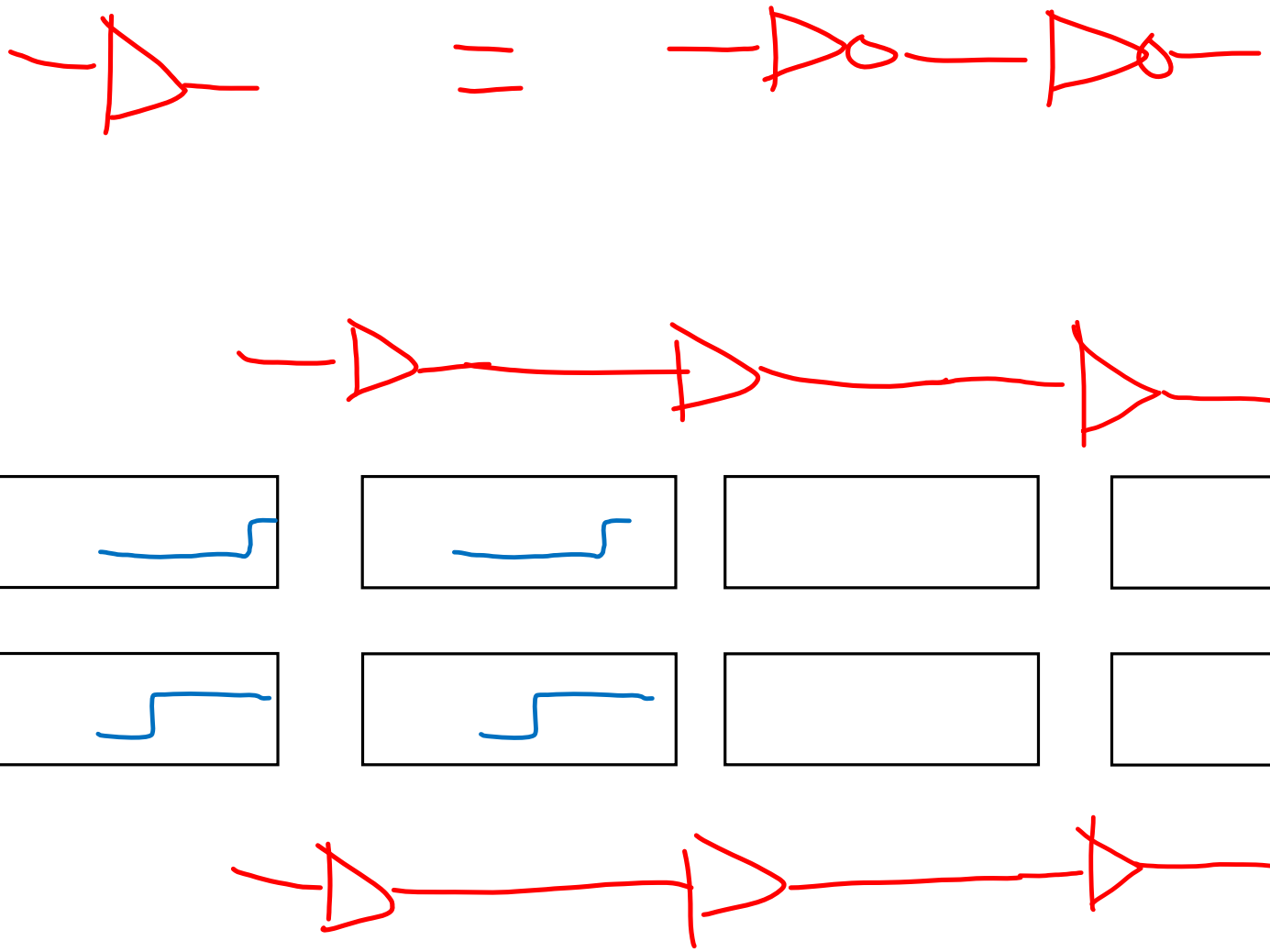
SMC array of 2048 elements

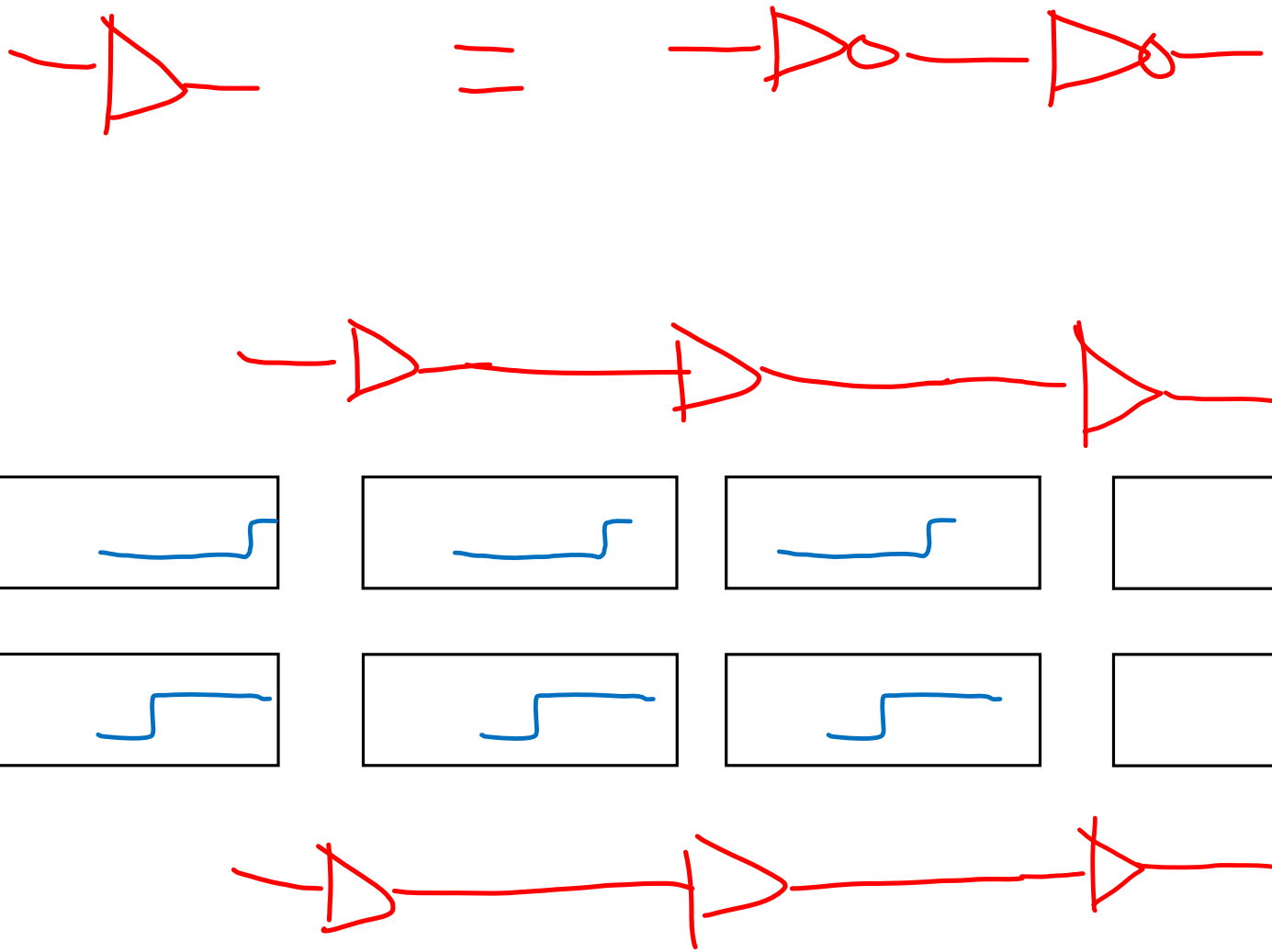


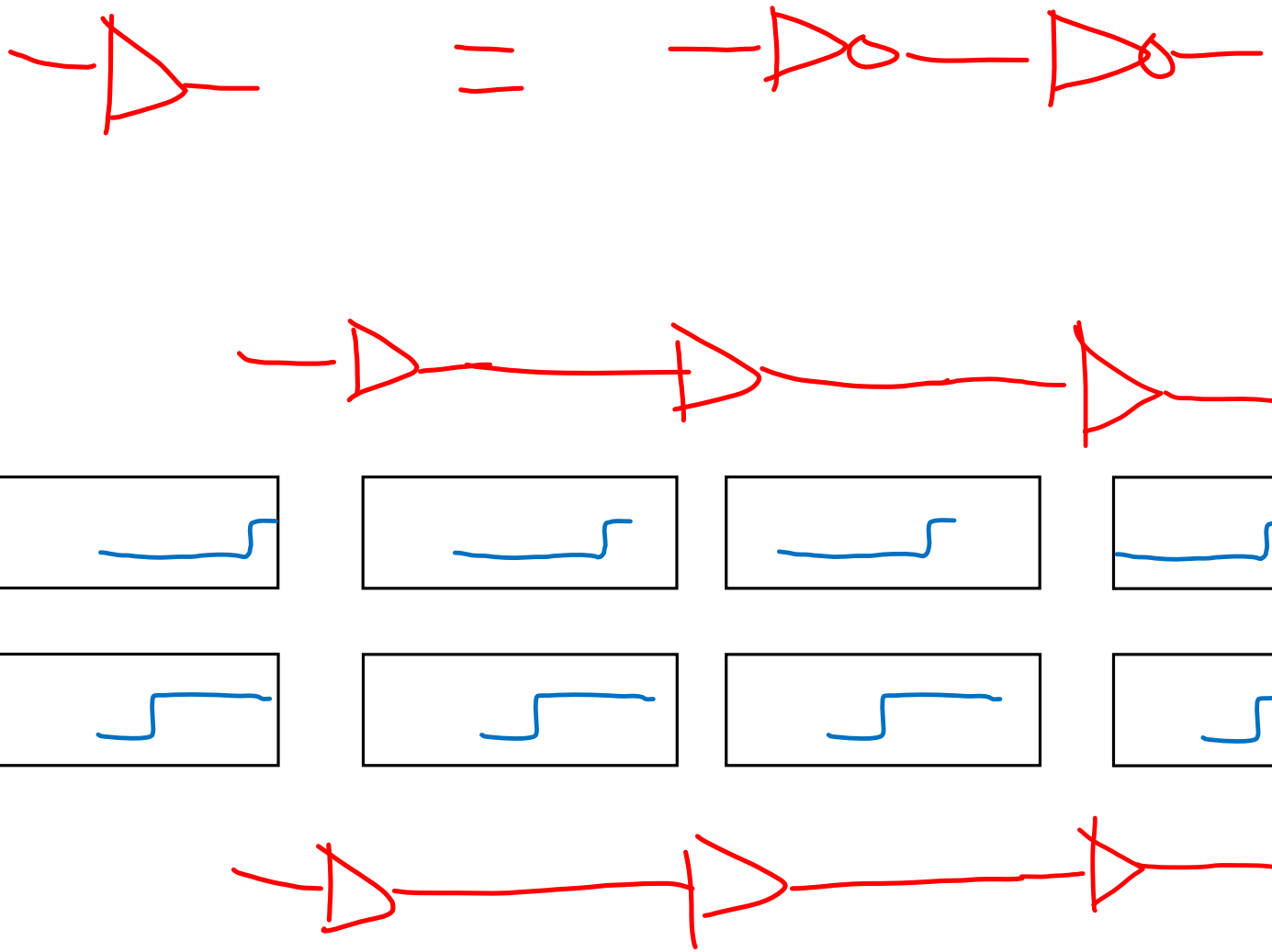
D. Ismari and J. Plusquellic, "IP-Level Implementation of a Resistance-Based Physical Unclonable Function", HOST, 2014.

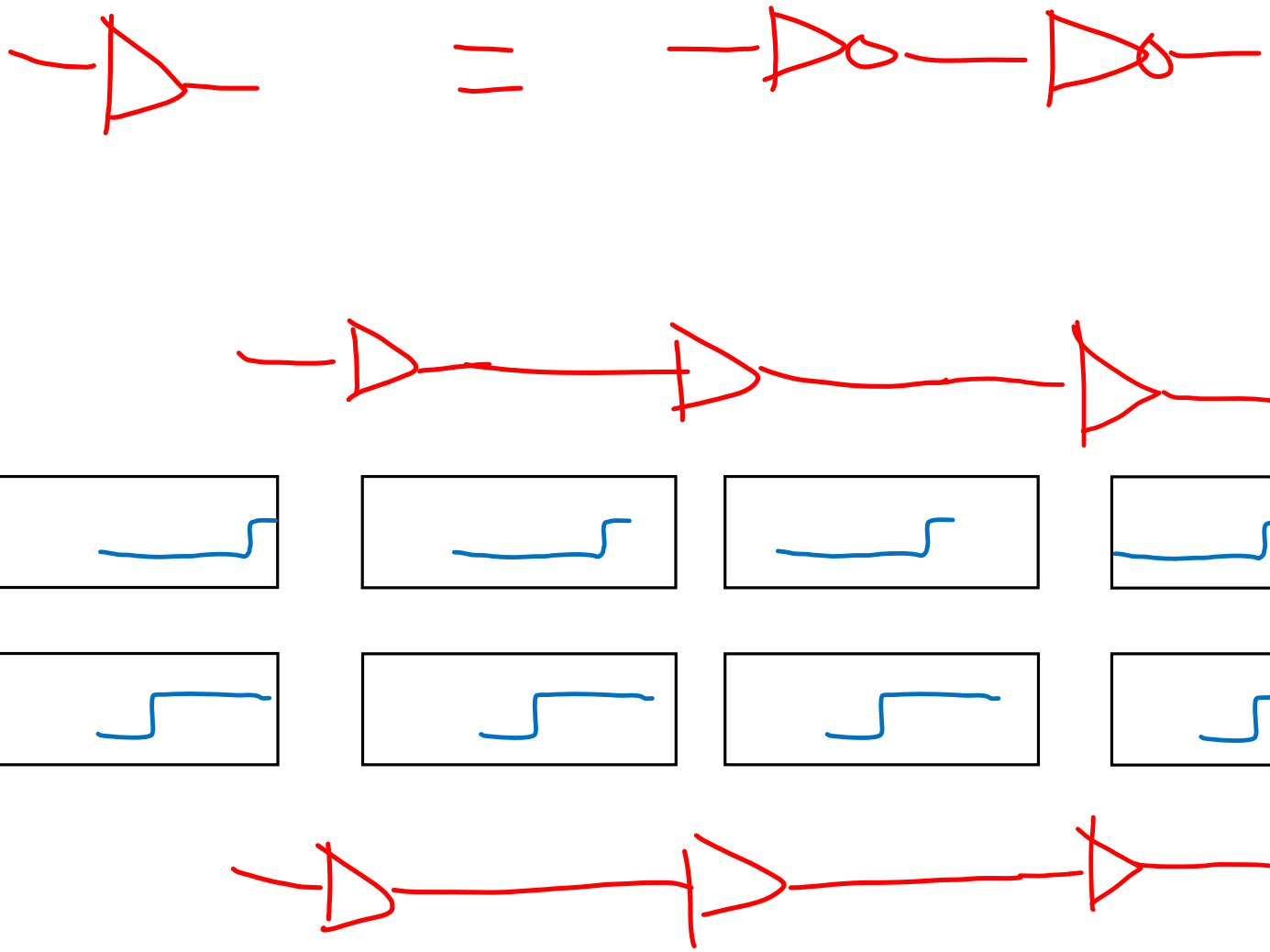
Voltage-to-digital-converter (VDC)

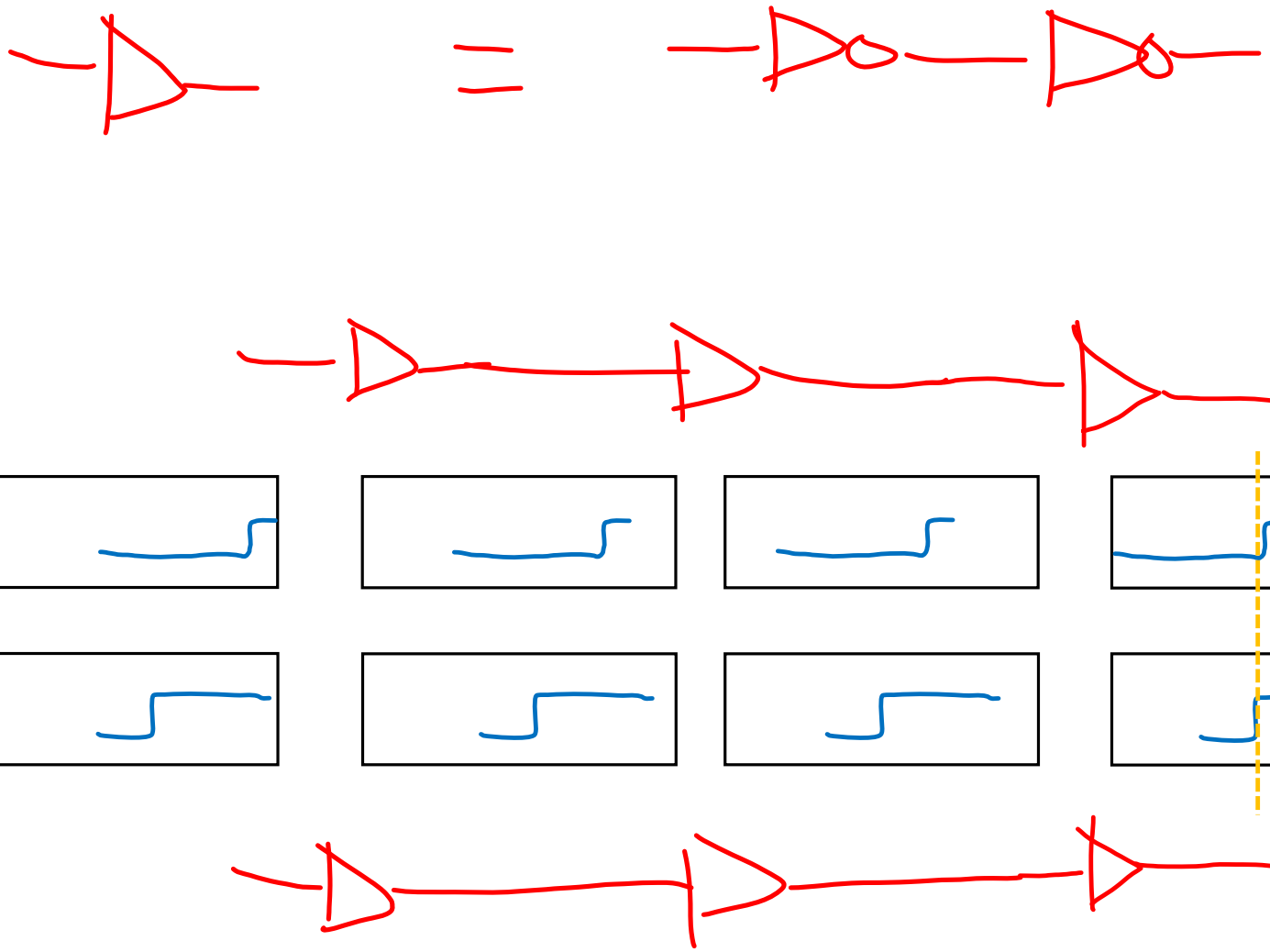




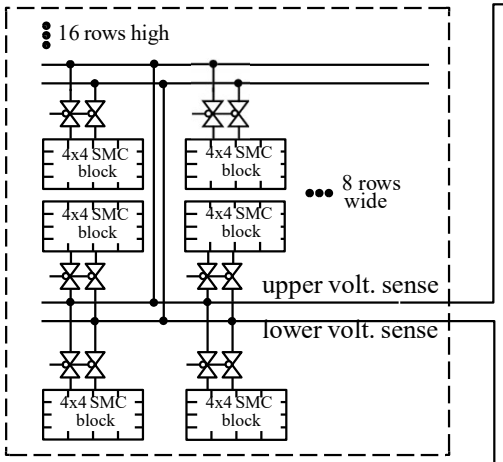








SMC array of 2048 elements



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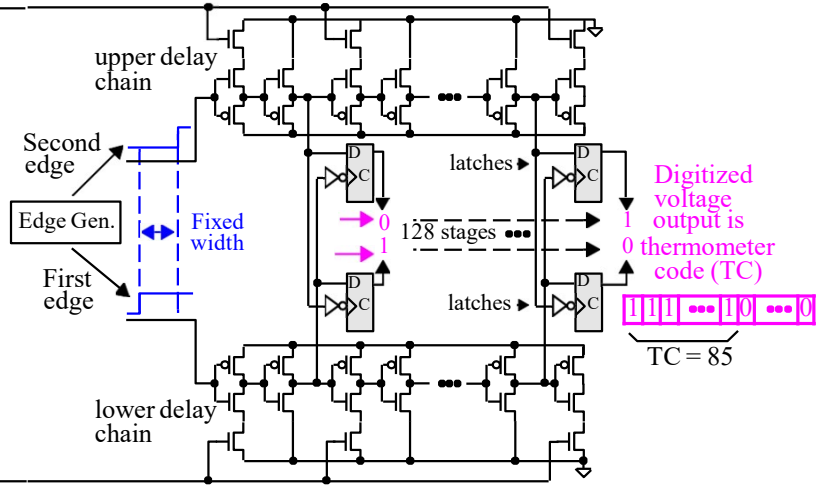
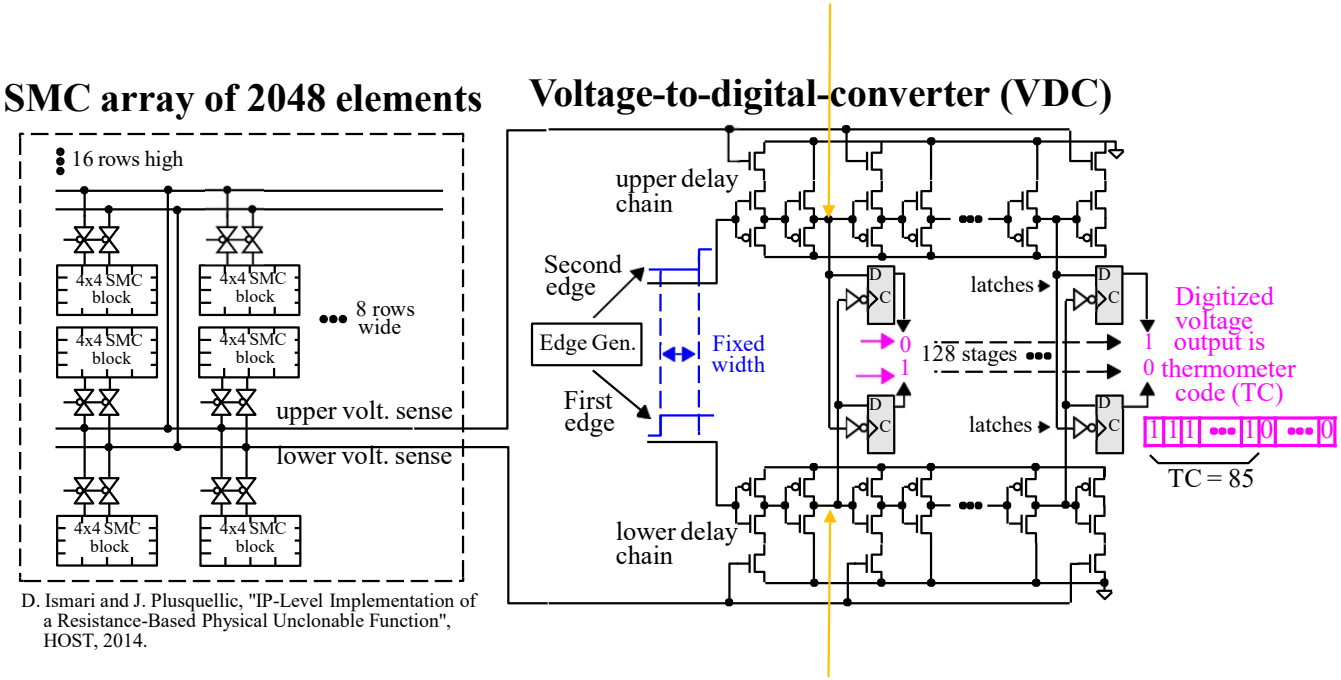
Voltage-to-digital-converter (VDC)





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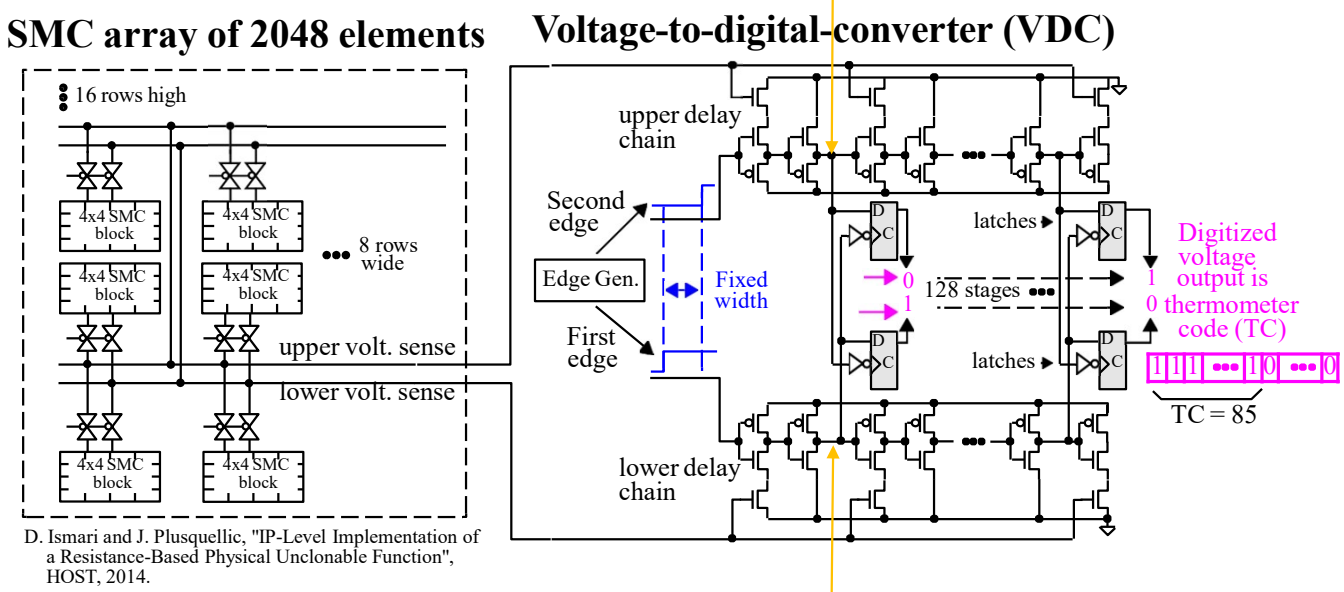
0 to 1 second



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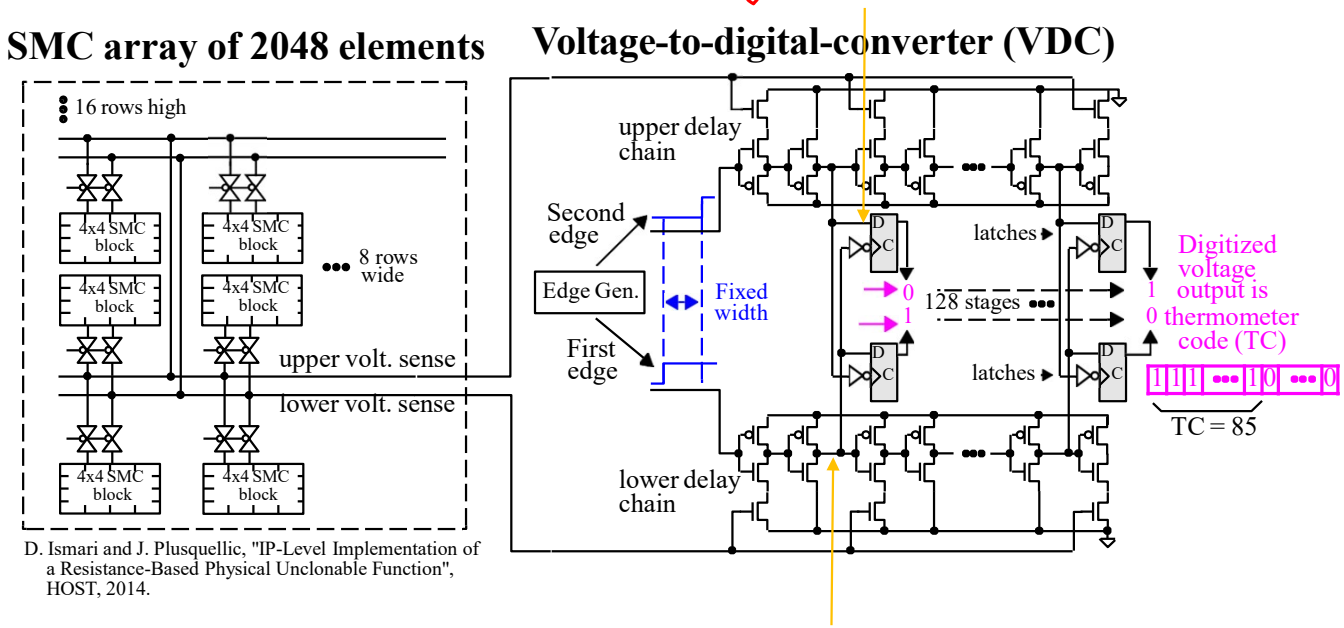

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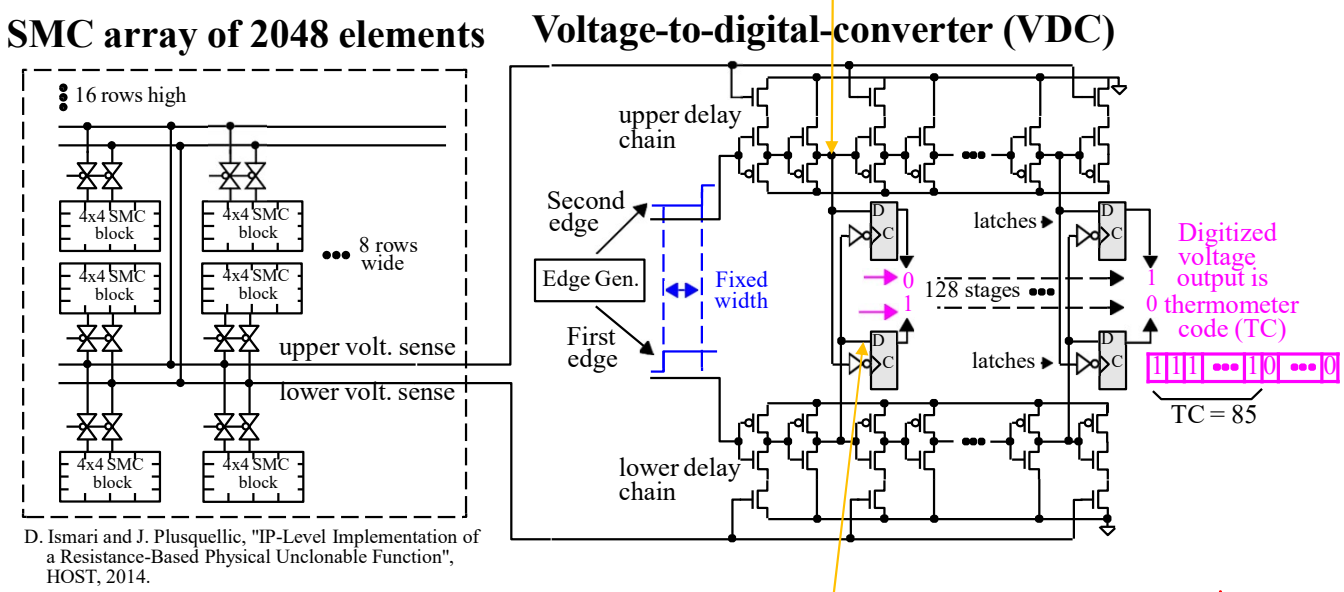

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1 captured second