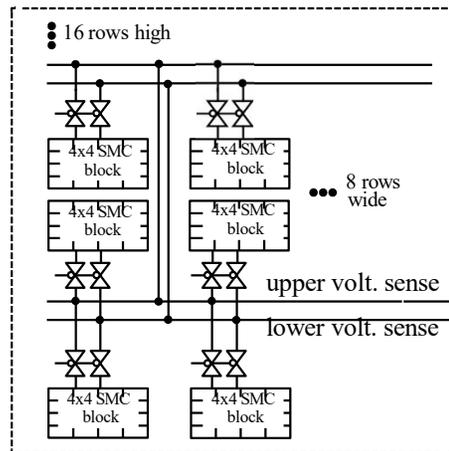
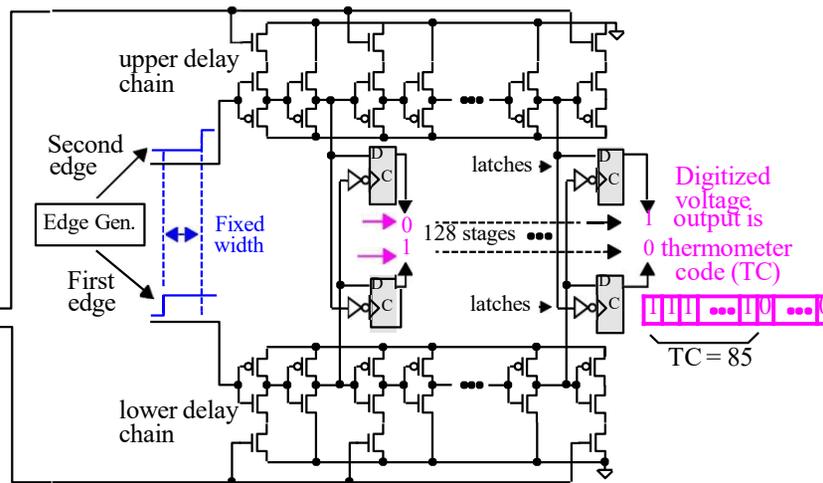


SMC array of 2048 elements

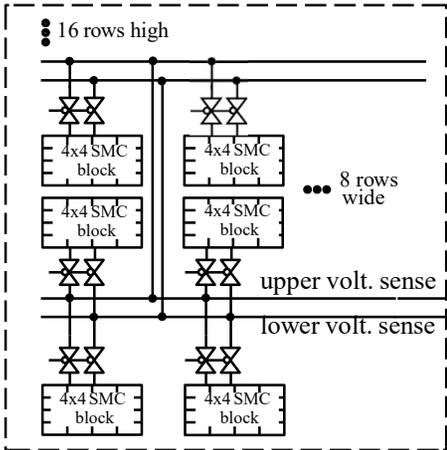


Voltage-to-digital-converter (VDC)

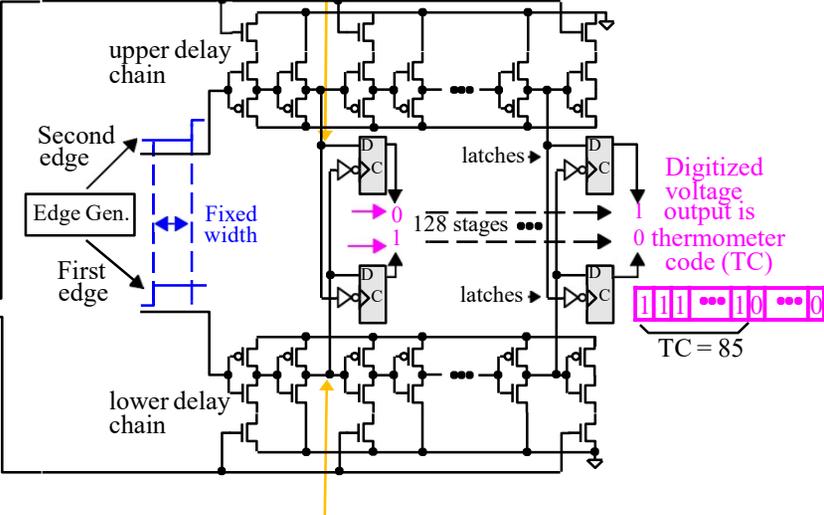


D. Ismari and J. Plusquellic, "IP-Level Implementation of a Resistance-Based Physical Unclonable Function", HOST, 2014.

SMC array of 2048 elements



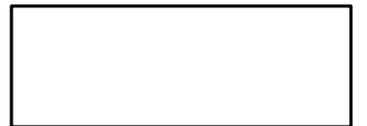
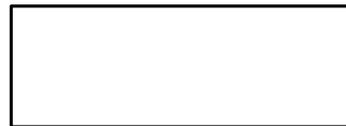
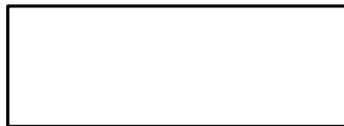
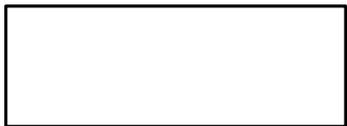
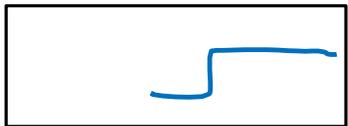
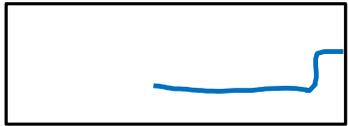
Voltage-to-digital-converter (VDC)



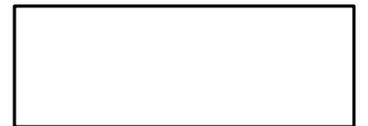
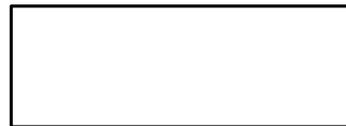
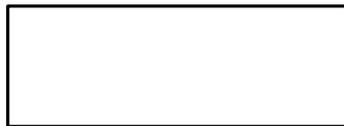
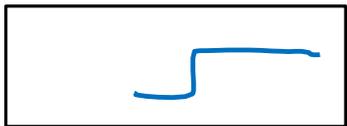
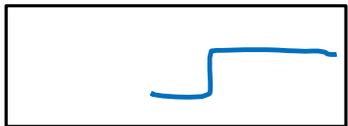
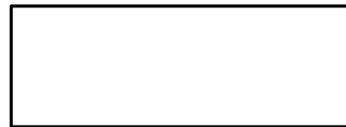
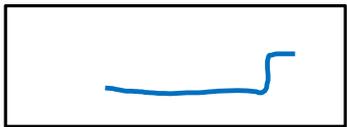
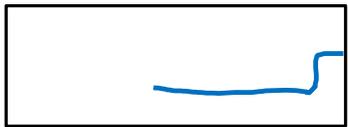
$$\neg \Delta = \neg \Delta \circ \neg \Delta$$



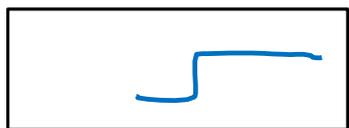
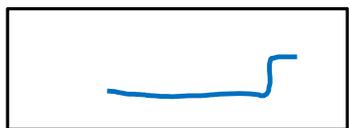
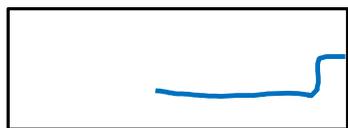
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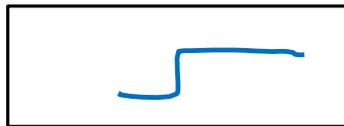
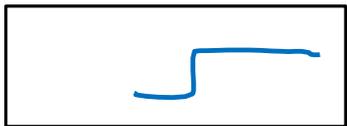
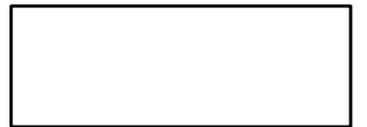
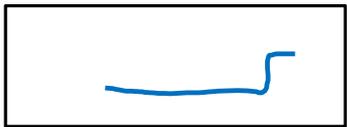
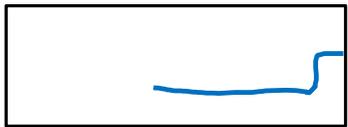
$$\neg \Delta = \neg \Delta \circ \neg \Delta$$



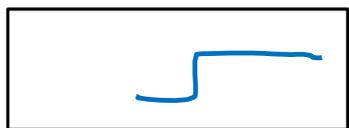
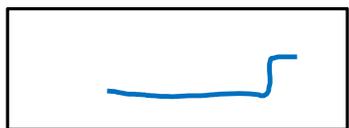
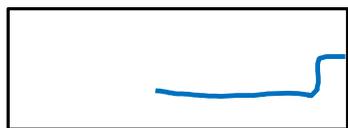
$$\neg \Delta = \neg \Delta \circ \neg \Delta$$



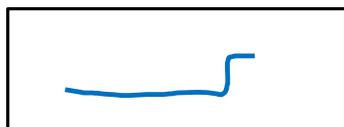
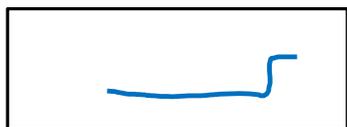
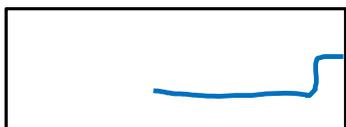
$$\neg \Delta = \neg \Delta \circ \neg \Delta$$



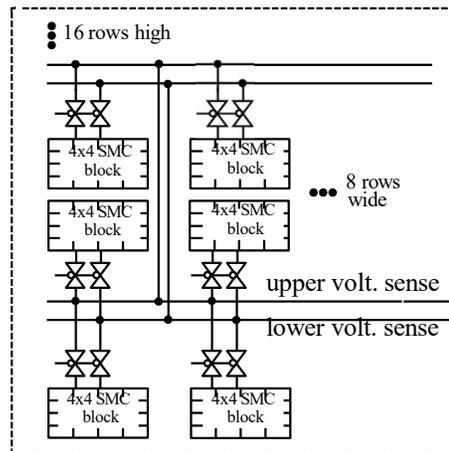
$$\text{---} \triangle \text{---} = \text{---} \triangle \circ \text{---} \triangle \circ \text{---}$$



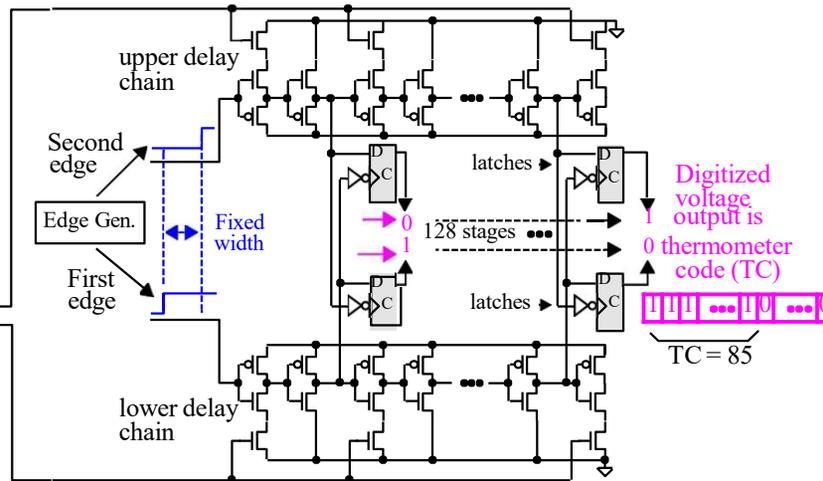
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SMC array of 2048 elements

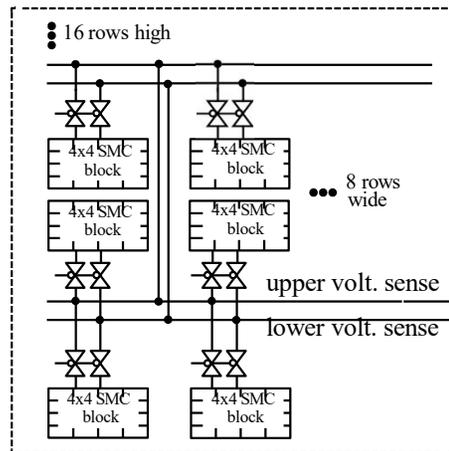


Voltage-to-digital-converter (VDC)

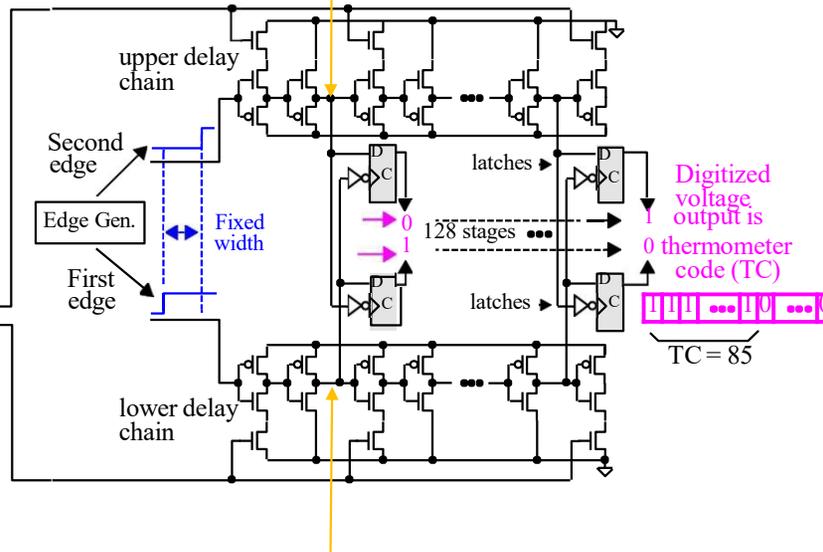


D. Ismari and J. Plusquellic, "IP-Level Implementation of a Resistance-Based Physical Unclonable Function", HOST, 2014.

SMC array of 2048 elements



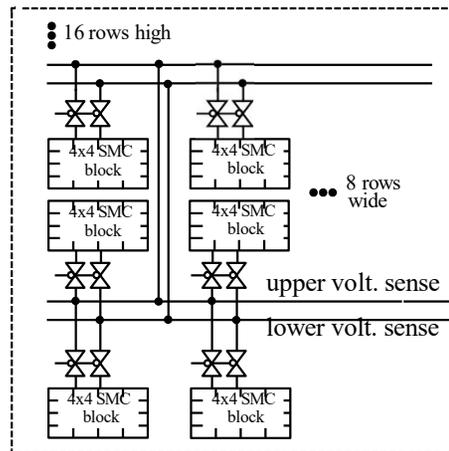
Voltage-to-digital-converter (VDC)



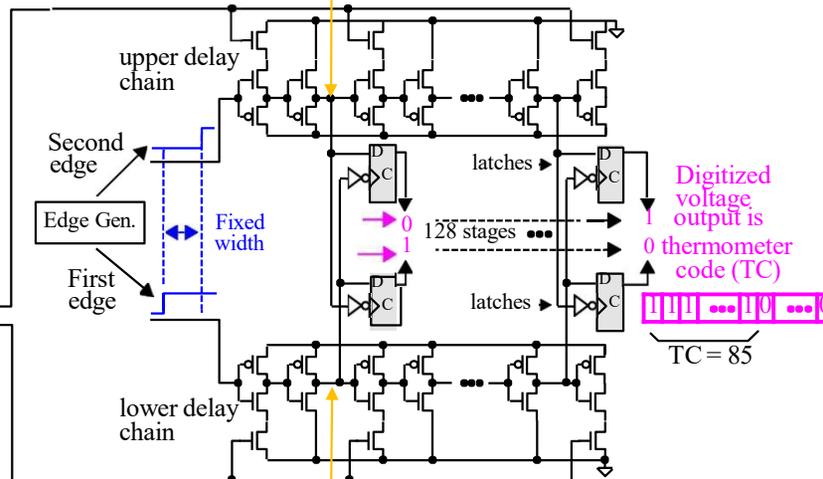
D. Ismari and J. Plusquellic, "IP-Level Implementation of a Resistance-Based Physical Unclonable Function", HOST, 2014.

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SMC array of 2048 elements



Voltage-to-digital-converter (VDC)

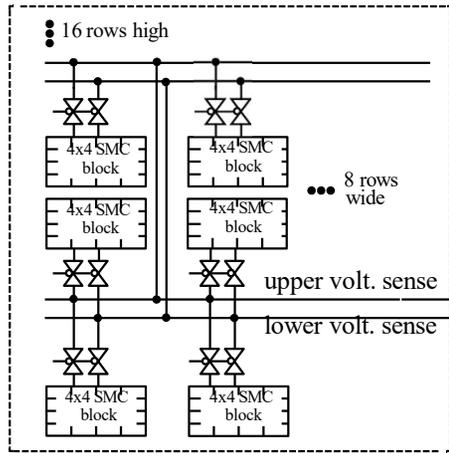


D. Ismari and J. Plusquellic, "IP-Level Implementation of a Resistance-Based Physical Unclonable Function", HOST, 2014.

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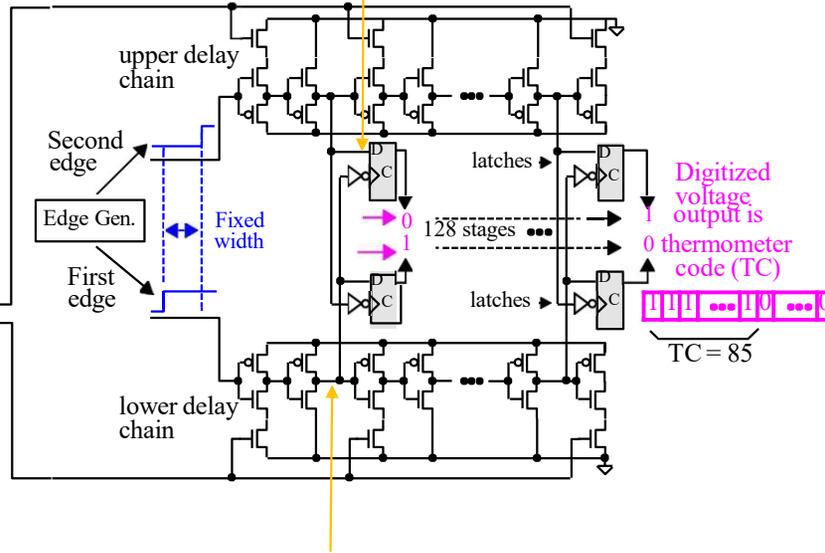
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SMC array of 2048 elements



D. Ismari and J. Plusquellic, "IP-Level Implementation of a Resistance-Based Physical Unclonable Function", HOST, 2014.

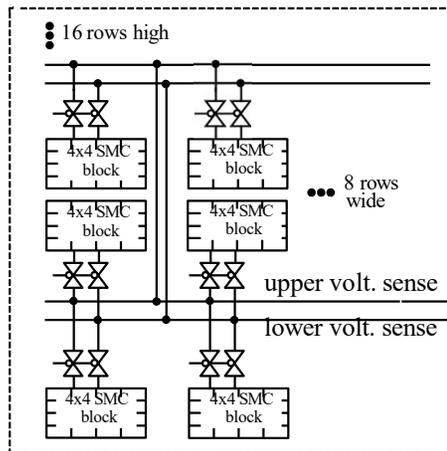
Voltage-to-digital-converter (VDC)



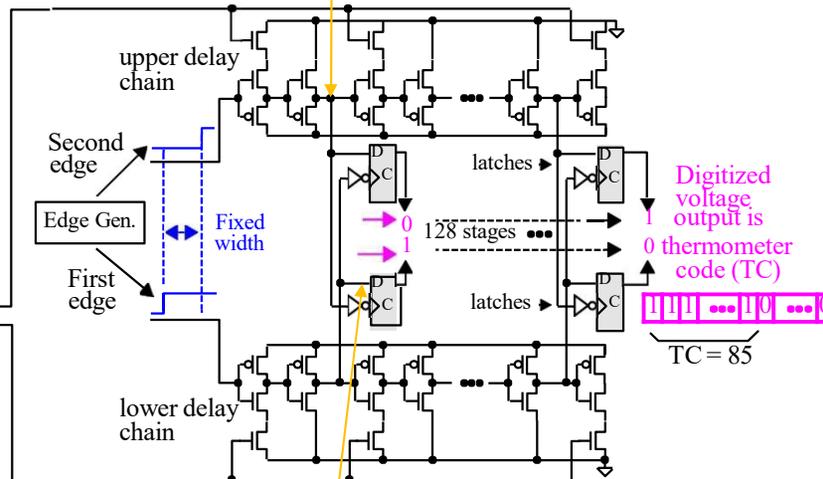
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SMC array of 2048 elements



Voltage-to-digital-converter (VDC)



D. Ismari and J. Plusquellic, "IP-Level Implementation of a Resistance-Based Physical Unclonable Function", HOST, 2014.

1 captured second