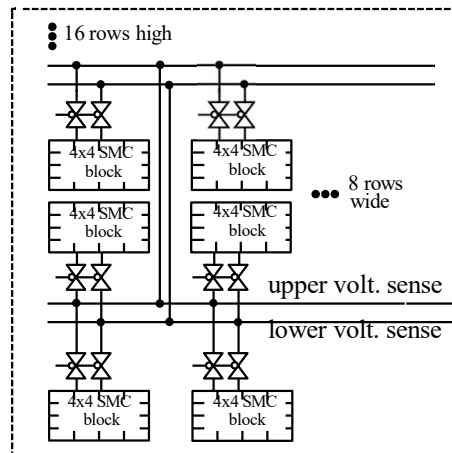
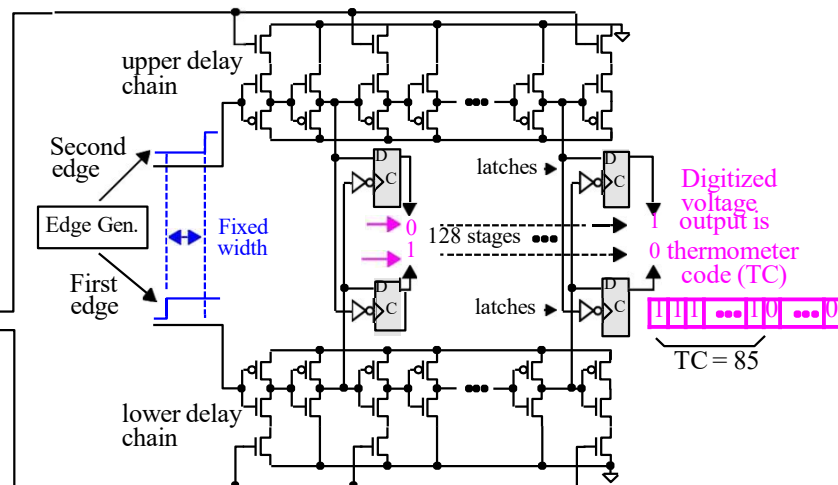


SMC array of 2048 elements

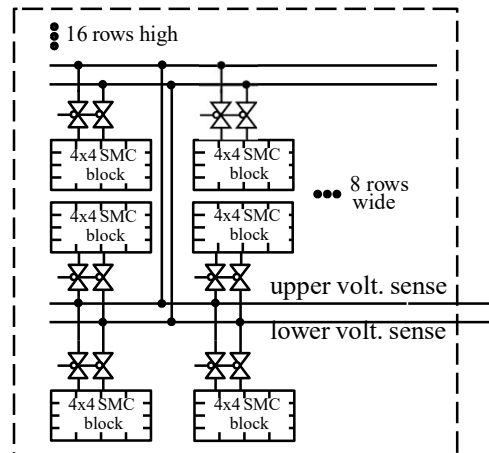


D. Ismari and J. Plusquellic, "IP-Level Implementation of a Resistance-Based Physical Unclonable Function", HOST, 2014.

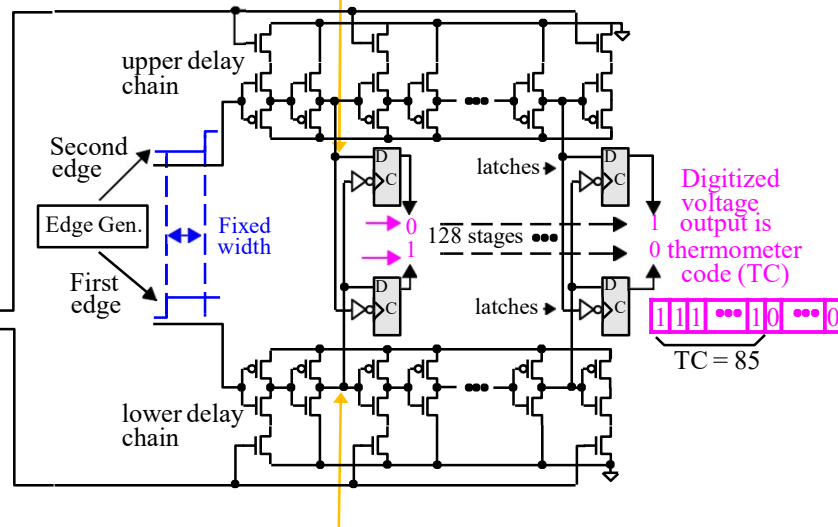
Voltage-to-digital-converter (VDC)



SMC array of 2048 elements



Voltage-to-digital-converter (VDC)



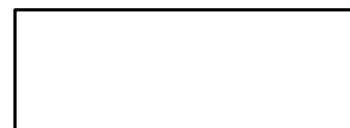
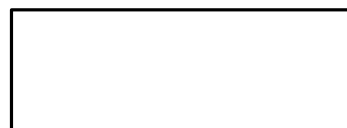
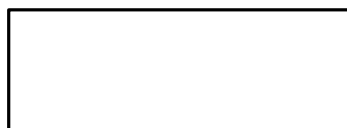
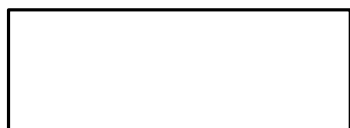
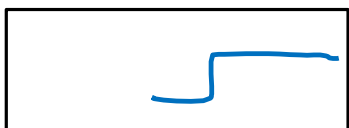
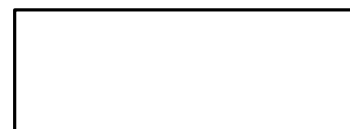
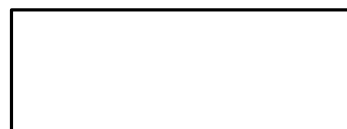
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$$-\triangleright-\triangleright-\triangleright-\triangleright-$$

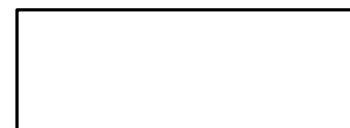
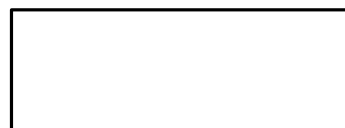
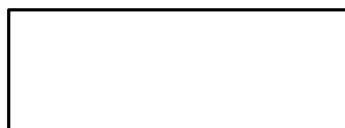
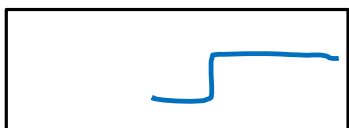
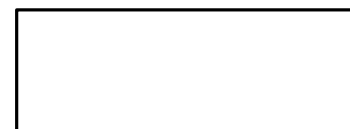
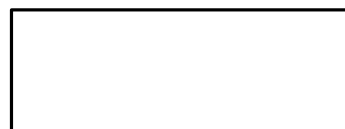
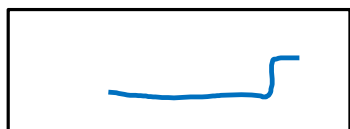


$$-\triangleright-\triangleright-\triangleright-\triangleright-$$

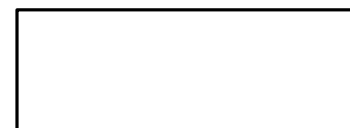
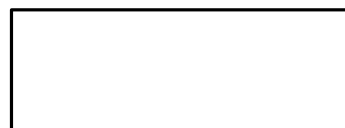
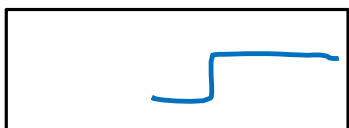
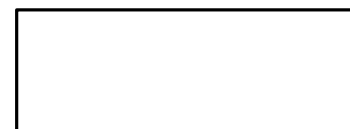
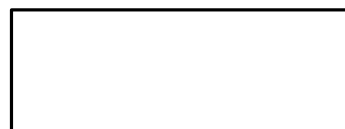
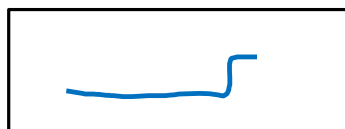
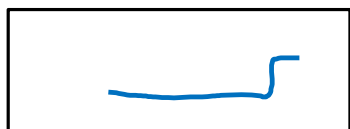
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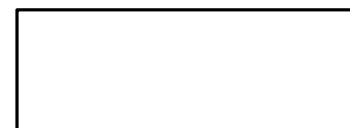
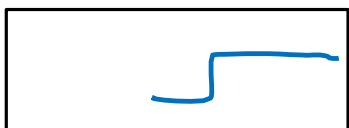
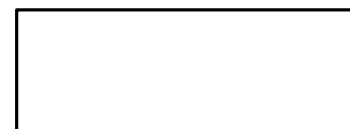
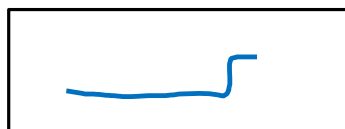
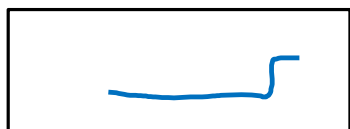
$$-\triangleright = -\triangleright\circ-\triangleright\circ-$$



$$\neg \triangleright = \neg \circ \neg \circ$$

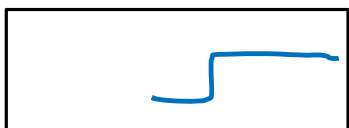
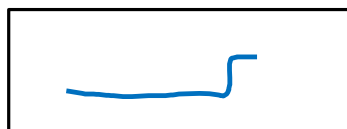
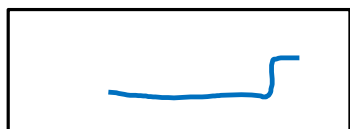


$$-\triangleright = -\circ-\circ-$$



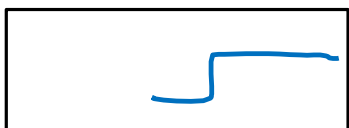
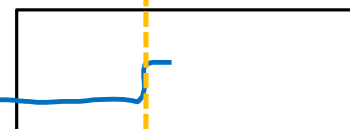
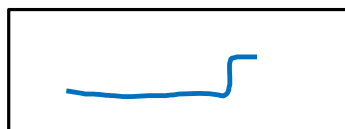
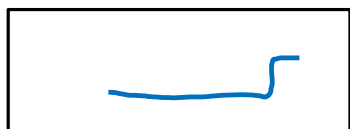
$$-\triangleright = -\triangleright\circ-\triangleright\circ-$$

$$-\triangleright-\triangleright-\triangleright-\triangleright-$$

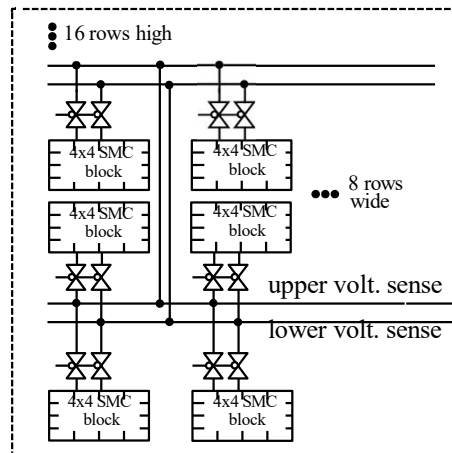


$$-\triangleright-\triangleright-\triangleright-\triangleright-$$

$$-\triangle = -\triangle\circ-\triangle\circ-$$

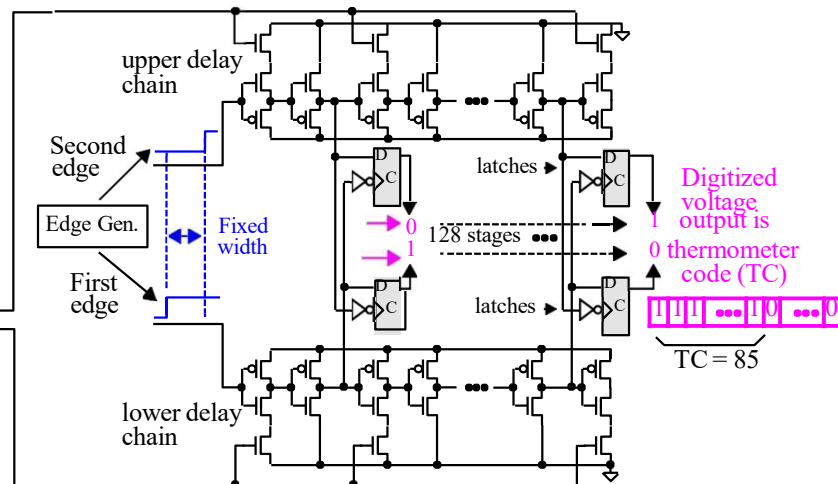


SMC array of 2048 elements

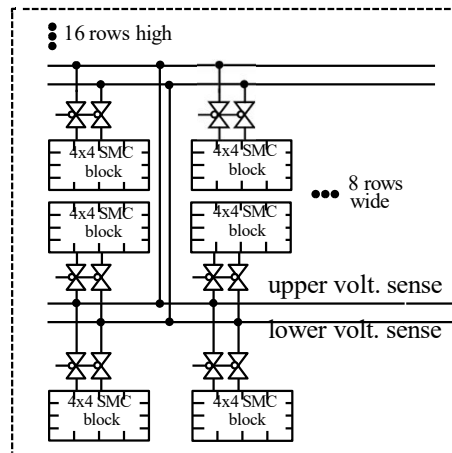


D. Ismari and J. Plusquellic, "IP-Level Implementation of a Resistance-Based Physical Unclonable Function", HOST, 2014.

Voltage-to-digital-converter (VDC)

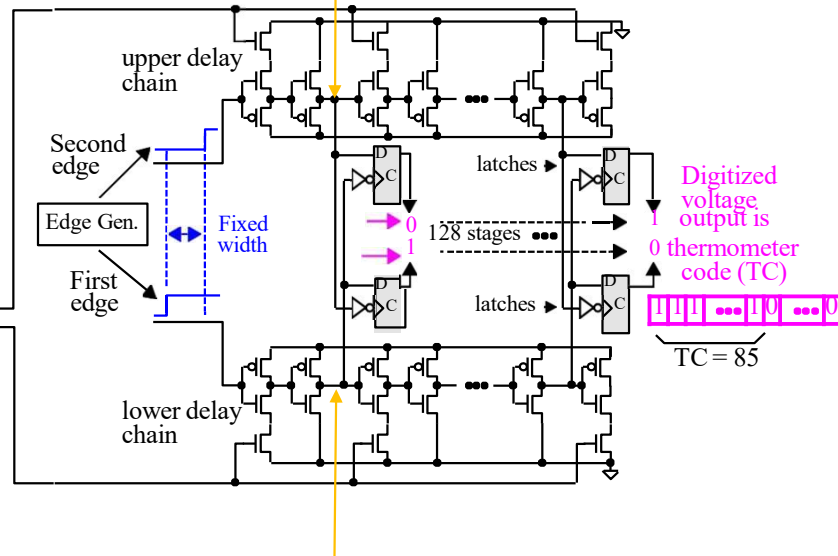


SMC array of 2048 elements



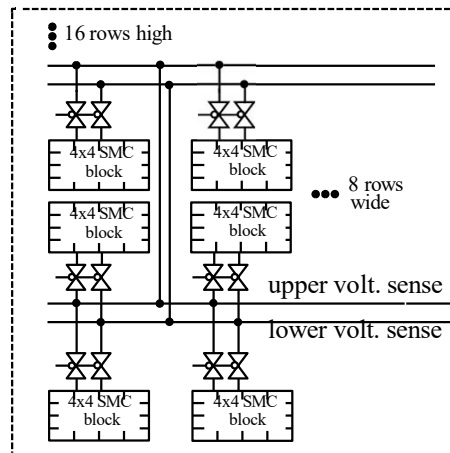
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Voltage-to-digital-converter (VDC)



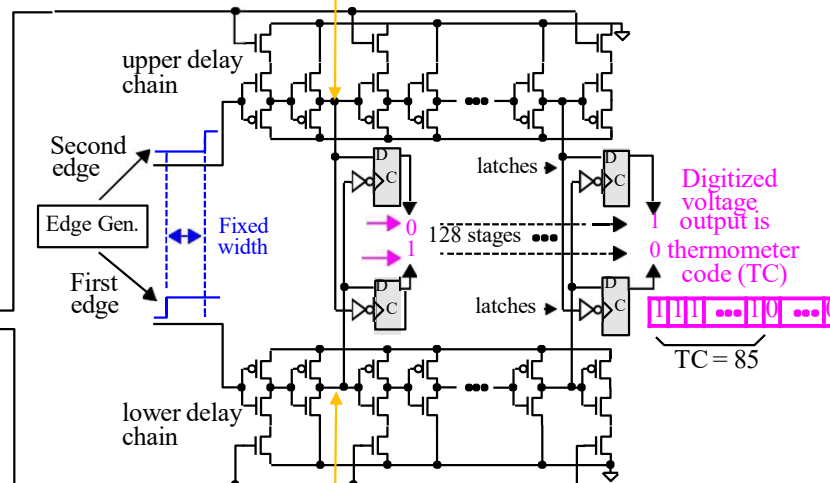
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SMC array of 2048 elements



D. Ismari and J. Plusquellic, "IP-Level Implementation of a Resistance-Based Physical Unclonable Function", HOST, 2014.

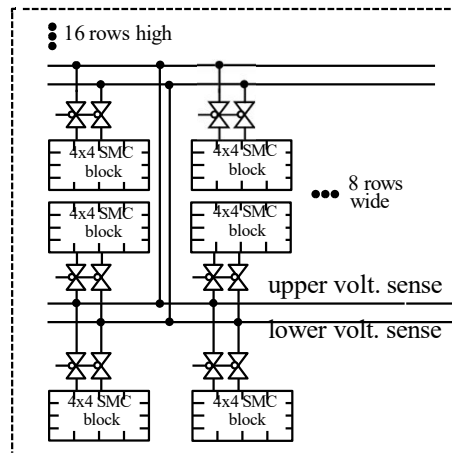
Voltage-to-digital-converter (VDC)



0 to 1 first

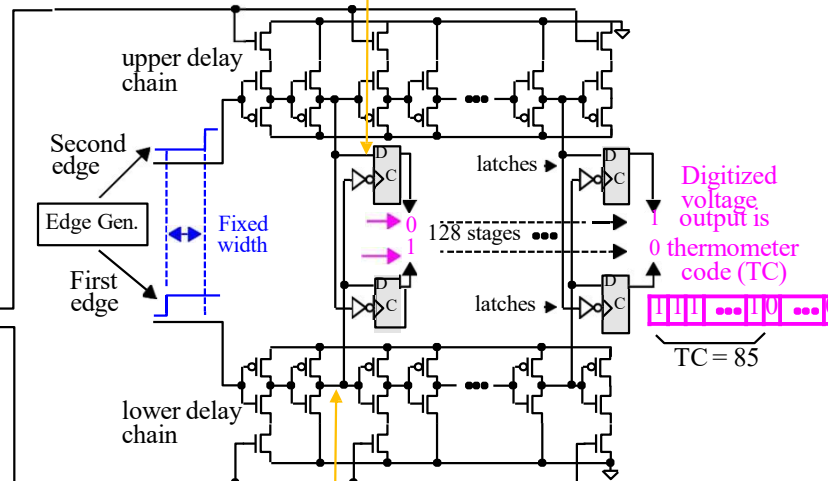
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SMC array of 2048 elements



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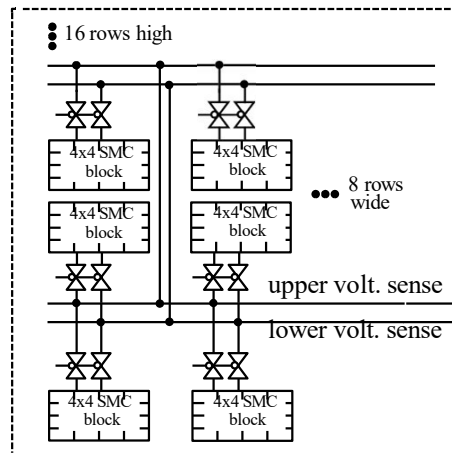
Voltage-to-digital-converter (VDC)



0 to 1 first

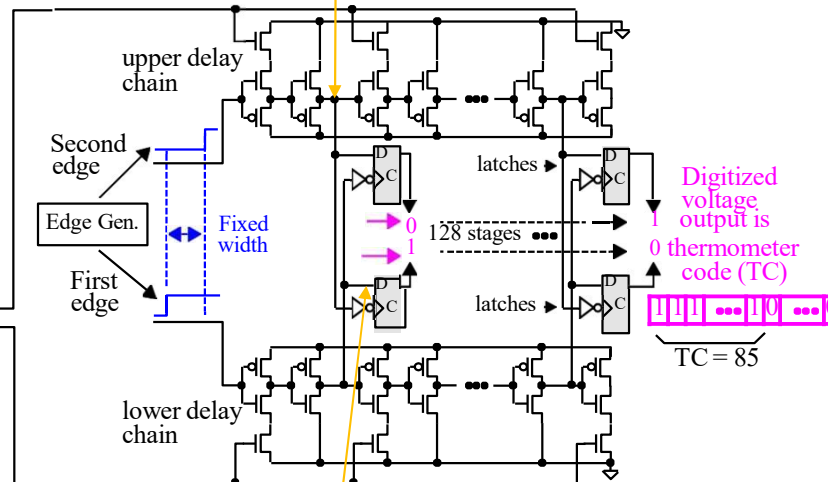
0 to 1 second

SMC array of 2048 elements



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Voltage-to-digital-converter (VDC)



1 captured second